

ZXTC2061E612V, SOT23-6, complementary medium power transistors

Summary

 $BV_{CEO} > 12 (-12)V$

 $h_{FE} > 500$

 $I_{C(cont)} = 5 (-3.5)A$

V_{CE(sat)} < 40 (-70)mV @ 1A

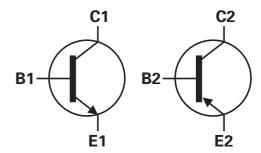
 $R_{CE(sat)} = 25 (45) m\Omega$

 $P_{D} = 1.1W$



Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT23-6 package provides a compact solution for the intended applications.





Features

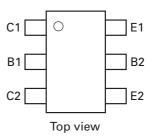
- NPN-PNP combination
- · Very low saturation voltage
- · High gain
- SOT23-6 package

Applications

- · MOSFET and IGBT gate driving
- Motor drive

Ordering information

DEVICE	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2061E6TA	7	8	3000



Device marking

2061

Absolute maximum and thermal ratings

PARAMETER	Symbol	Limit	Unit
Collector-base voltage	V _{CBO}	20(-12)	V
Collector-emitter voltage	V _{CEO}	12(-12)	V
Emitter-base voltage	V _{EBO}	7(-7)	V
Continuous collector current ^{(c)(f)}	I _C	5(-3.5)	Α
Peak pulse current	I _{CM}	12(-10)	Α
Base current	I _B	1(-1)	А
Power dissipation at T _A =25°C ^{(a)(f)}	P _D	0.7	W
Linear derating factor		5.6	mW/°C
Power dissipation at T _A =25°C ^{(b)(f)}	P _D	0.9	W
Linear derating factor		7.2	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(b)(g)}$	P _D	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at T _A =25°C ^{(c)(f)}	P _D	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at T _A =25°C ^{(d)(f)}	P _D	1.7	W
Linear derating factor		13.6	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150	°C
Thermal resistance junction to ambient ^{(a)(f)}	$R_{\theta JA}$	179	°C/W
Thermal resistance junction to ambient ^{(b)(f)}	$R_{ heta JA}$	139	°C/W
Thermal resistance junction to ambient ^{(b)(g)}	$R_{\theta JA}$	113	°C/W
Thermal resistance junction to ambient ^{(c)(f)}	$R_{ heta JA}$	113	°C/W
Thermal resistance junction to ambient ^{(d)(f)}	$R_{\theta JA}$	73	°C/W

NOTES:

⁽a) For a device surface mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽b) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽c) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

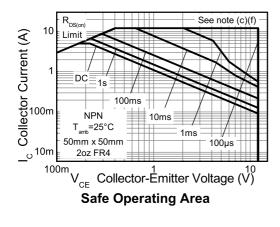
⁽d) As above measured at t<5 seconds.

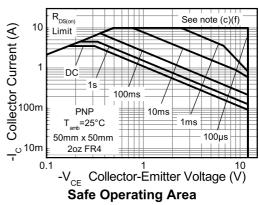
⁽e) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

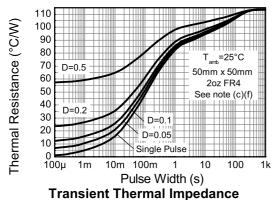
⁽f) For device with one active die, both collectors attached to a common sink.

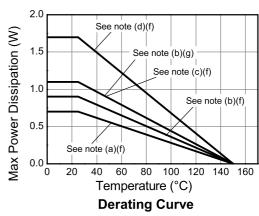
⁽g) For device with two active dice running at equal power, split sink 50% to each collector.

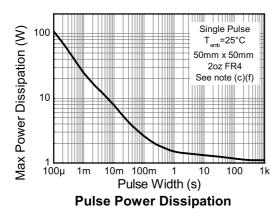
Thermal characteristics











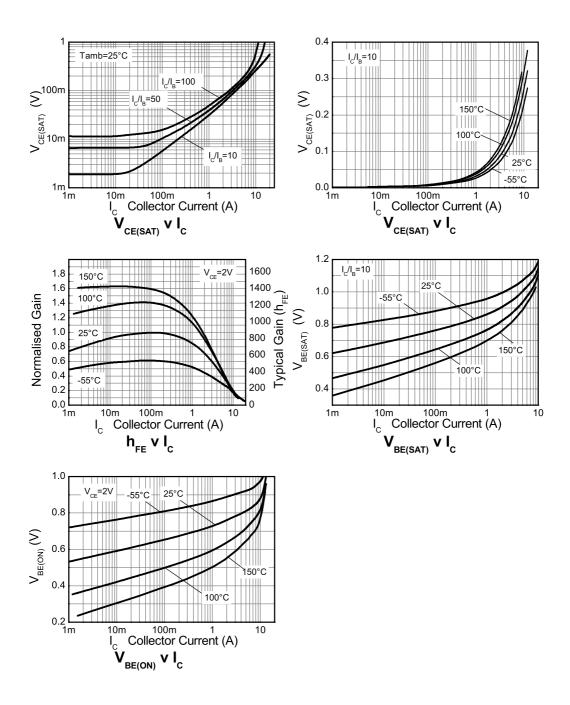
ELECTRICAL CHARACTERISTICS (at Tamb = 25°C unless otherwise stated).

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base	BV _{CBO}	20(-12)	40(-35)		V	$I_C = (-)10 \mu A$	
breakdown voltage							
Collector-emitter	BV _{CEO}	(-)12	17(-25)		V	I _C = (-)10mA ^(*)	
breakdown voltage							
(base open)							
Emitter-base	BV _{EBO}	(-)7	(-)8.4		V	$I_E = (-)100 \mu A$	
breakdown voltage							
Collector-base cut-off	I _{CBO}		<1	(-)50	nA	V _{CB} =20(-12)V	
current				(-)0.5	μΑ	V _{CB} =20(-12)V, T _{amb} = 100°C	
Emitter-base cut-off	I _{EBO}		<1	(-)50	nA	$V_{EB} = (-)5.6V$	
current							
Collector-emitter	V _{CE(sat)}		32(-55)	40(-70)	m۷	$I_C = (-)1A, I_B = (-)100mA^{(*)}$	
saturation voltage			50(-170)	60(-265)	mV	$I_C = (-)1A, I_B = (-)10mA^{(*)}$	
			65(-220)	80(-360)	mV	$I_C = (-)2A, I_B = (-)40mA^{(*)}$	
			(-150)	(-200)	mV	$(I_C = -3.5A, I_B = -350mA)^{(*)}$	
			145	180	mV	$I_C = 5A$, $I_B = 100mA^{(*)}$	
Base-emitter	V _{BE(sat)}		(-955)	(-1050)	mV	$(I_C = -3.5A, I_B = -350mA^{(*)})$	
saturation voltage			920	1000	mV	$I_C = 5A$, $I_B = 100mA^{(*)}$	
Base-emitter turn-on	V _{BE(on)}		(-830)	(-900)	mV	$(I_C = -3.5A, V_{CE} = -2V^{(*)})$	
voltage			810	900	mV	$I_C = 5A$, $V_{CE} = 2V^{(*)}$	
Static forward current	h _{FE}	500(500)	800(800)	1500(1500)		I _C = (-)10mA, V _{CE} = (-)2V ^(*)	
transfer ratio		480(290)	750(450)			$I_C = (-)1A, V_{CE} = (-)2V^{(*)}$	
		(75)	(100)			$(I_C = -3.5A, V_{CE} = -2V^{(*)})$	
		260	390			$I_C = 5A$, $V_{CE} = 2V^{(*)}$	
Transition frequency	f _T		260		MHz	I _C = (-)50mA, V _{CE} = (-)10V	
			(310)			f = 100MHz	
Output capacitance	C _{OBO}		26(17)	35(25)	рF	V _{CB} = (-)10V, f = 1MHz ^(*)	
Delay time	t _d		71(41)		ns	., ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Rise time	t _r		70(62)		ns	$V_{CC} = (-)10V. I_C = (-)1A,$ $I_{B1} = -I_{B2} = (-)10mA.$	
Storage time	t _s		233(179)		ns		
Fall time	t _f		72(65)		ns		

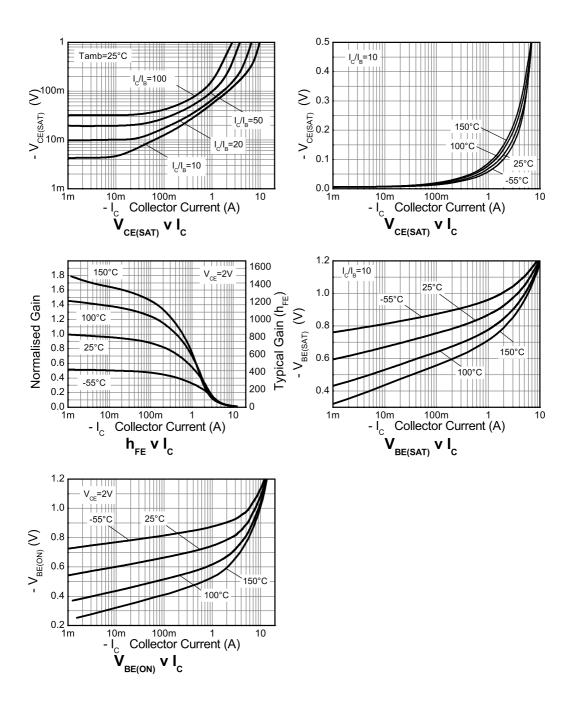
NOTES:

^(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$ () = PNP

NPN electrical characteristics

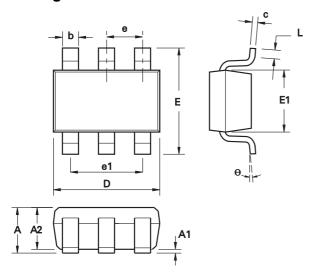


PNP electrical characteristics

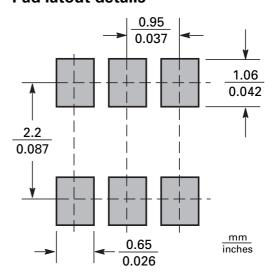


Package outline SOT23-6

Package outline



Pad latout details



DIM	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
Α	0.90	1.45	0.354	0.0570	
A1	0.00	0.15	0.00	0.0059	
A2	0.90	1.30	0.0354	0.0511	
b	0.35	0.50	0.0078	0.0196	
С	0.09	0.26	0.0035	0.0102	
D	2.70	3.10	0.1062	0.1220	
E	2.20	3.20	0.0866	0.1181	
E1	1.30	1.80	0.0511	0.0708	
L	0.10	0.60	0.0039	0.0236	
е	0.95 REF		0.0374 REF		
e1	1.90 REF		0.074	0.0748 REF	
L	0°	30°	0°	30°	

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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